

# 25G SFP28 850nm Active Octical Cable Multi Mode PSFP28-AOCXX

#### **Features**

- ♦ Hot-pluggable SFP28 form factor
- ♦ Supports 25Gbps data rate
- ♦ Maximum link length of 70m on OM3 MMF and 100m on OM4 MMF
- ♦ 850nm VCSEL laser and PIN photo-detector
- ♦ Internal CDR on both Transmitter and Receiver channel
- ♦ Single 3.3V power supply
- ♦ Power dissipation < 1W
- ♦ Digital diagnostics functions are available via the I2C interface
- ♦ RoHS-6 compliant
- ♦ Commercial case temperature range: 0°C to 70°C

## **Applications**

♦ 25GBASE-SR Ethernet

## **Ordering information**

Part Number	Product Description
PSFP28-AOCXX	SFP28 25Gbps, Active Optical Cable, 850nm MMF (XX=Cable length in meters)

### Overview

SFP28 Active Optical Cables are direct-attach fiber assemblies with SFP28 connectors. They are suitable for very short distances and offer a cost-effective way to connect within racks and across adjacent racks.

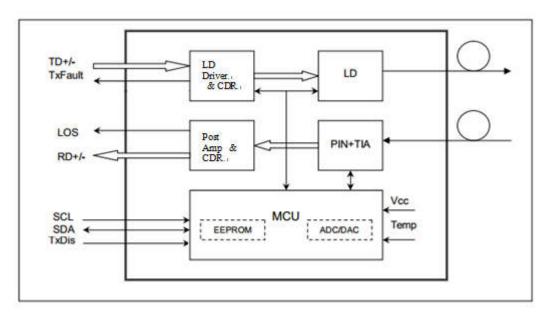
PSFP28-AOCXX is a single-Channel, Pluggable, Fiber-Optic SFP28 for 25 Gigabit Ethernet and Infiniband EDR Applications. It is a high performance module for short-range data communication and interconnect applications which operate at 25.78125 Gbps up to 70 m using OM3 fiber or 100 m using OM4 fiber. This module is designed to operate over multimode fiber systems using a nominal wavelength of 850nm. The electrical interface uses a 20 contact edge type connector.

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### PSFP28-AOCXX

## **Block Diagram**



Block Diagram

## **Absolute Maximum Ratings**

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	0	3.6	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

## **Recommended Operating Conditions**

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Тс	0		+70	°C
Power Supply Voltage	Vcc	3.13	3.3	3.47	V
Power Supply Current	Icc			300	mA
Fiber Length on 50/125 $\mu$ m high-bandwidth (OM3) MMF				70	m
Fiber Length on 50/125μm high-bandwidth (OM4) MMF				100	m



## **Optical and Electrical Characteristics**

Parar	meter	Symbol	Min	Typical	Max	Unit	Notes
			Transmitte	r			
Data	rate	BR		25.78		Gbps	
Centre Wa	avelength	λc	840	850	860	nm	
Spectral Wi	dth (-20dB)	σ			0.6	nm	
Average Ou	tput Power	Pavg	-8.4		2.4	dBm	
Optical Po	ower OMA	P <sub>OMA</sub>	-6.4		3	dBm	
Extinction	on Ratio	ER	2			dB	
Differential da	ta input swing	$V_{IN,PP}$	40		1000	mV	
Input Different	tial Impedance	Z <sub>IN</sub>	90	100	110	Ω	
	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
TV 5 1:	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Receiver				
Data	rate	BR		25.78		Gbps	
Centre Wa	avelength	λς	840	850	860	nm	
Receiver Sens	sitivity (OMA)	Psens	-	-	-10	dBm	
Stressed Sens	sitivity (OMA)		-	-	-5.2	dBm	
Receiver Power (OMA)					3	dBm	
LOS De-Assert		LOS <sub>D</sub>			-13	dBm	
LOS Assert		LOS <sub>A</sub>	-30			dBm	
LOS Hysteresis			0.5			dB	
Differential data output swing		Vout,PP	300		850	mV	
LOS		High	2.0		Vcc	V	
		Low			0.8	V	

#### Note:

1. Receive Sensitivity measured with a PRBS 31 pattern @25.78125Gbps, BER 1E-5;

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## **Timing and Electrical**

Parameter	Symbol	Min	Max	Unit	Conditions
Tx_Disable assert time	t_off		100	μs	Rising edge of Tx_Disable to fall of output signal below 10% of nominal
Tx_Disable negate time	t_on		2	ms	Falling edge of Tx_Disable to rise of output signal above 90% of nominal. This only applies in normal operations, not during start up or fault recovery.
Time to initialize 2-wire interface	t_2w_start_up		300	ms	From power on or hot plug after the supply meeting <u>Table 8</u> .
Time to initialize	t_start_up		300	ms	From power supplies meeting <u>Table 8</u> or hot plug or Tx disable negated during power up, or Tx_Fault recovery, until non-cooled power level I part (or non-cooled power level II part already enable at power level II for Tx_Fault recovery) is fully operational.
Time to initialize cooled module and time to power up a cooled module to Power Level II	t_start_up_cooled		90	S	From power supplies meeting <u>Table 8</u> or hot plug, or Tx disable negated during power up or Tx_Fault recovery, until cooled power level I part (or cooled power level II part during fault recovery) is fully operational. Also, from stop bit low-to-high SDA transition enabling Power Level II until cooled module is fully operational
Time to Power Up to Level II	t_power_level2		300	ms	From stop bit low-to-high SDA transition enabling power level II until non-cooled module is fully operational
Time to Power Down from Level II	t_power_down		300	ms	From stop bit low-to-high SDA transition dis- abling power level II until module is within power level I requirements
Tx_Fault assert	Tx_Fault_on		1	ms	From occurrence of fault to assertion of Tx_Fault
Tx_Fault assert for cooled module	Tx_Fault_on_cooled		50	ms	From occurrence of fault to assertion of Tx_Fault
Tx_Fault Reset	t_reset	10		μs	Time Tx_Disable must be held high to reset Tx_Fault
RSO, RS1 rate select timing for FC	t_RS0_FC, t_RS1_FC		500	μs	From assertion till stable output
RSO, RS1 rate select timing non FC	t_RS0, t_RS1		24	ms	From assertion till stable output
Rx_LOS assert delay	t_loss_on		100	μs	From occurrence of loss of signal to assertion of Rx_LOS
Rx_LOS negate delay	t_loss_off		100	μs	From occurrence of presence of signal to negation of Rx_LOS

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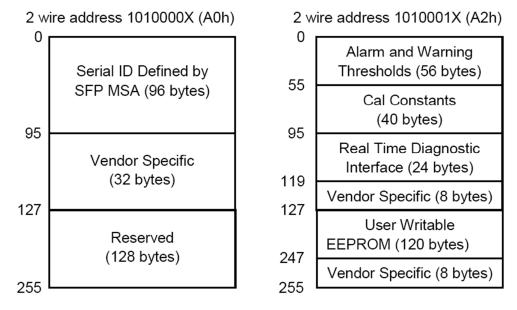
### PSFP28-AOCXX

## **Digital Diagnostic Memory Map**

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Digital Diagnostic Memory Map

## **Pin Descriptions**

PIN	Logic	Symbol	Name / Description	Note
1		VeeT	Module Transmitter Ground	1
2	LVTTL-O	TX_Fault	Module Transmitter Fault	2
3	LVTTL-I	TX_Dis	Transmitter Disable; Turns off transmitter laser output	
4	LVTTL-I/O	SDA	2-Wire Serial Interface Data Line	2
5	LVTTL-I	SCL	2-Wire Serial Interface Clock	2
6		MOD_ABS	Module Definition, Grounded in the module	
7	LVTTL-I	RS0	Receiver Rate Select	
8	LVTTL-O	RX_LOS	Receiver Loss of Signal Indication Active LOW	
9	LVTTL-I	RS1	Transmitter Rate Select (not used)	
10		VeeR	Module Receiver Ground	1

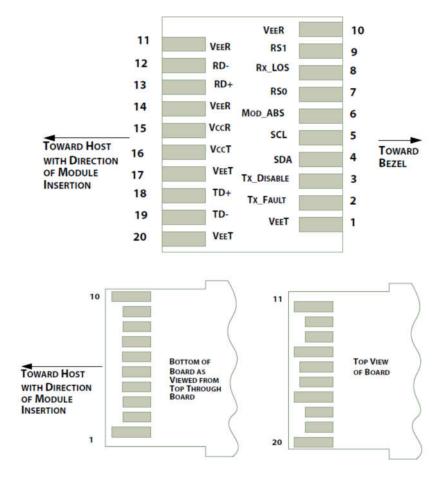


11		VeeR	Module Receiver Ground	1
12	CML-O	RD-	Receiver Inverted Data Output	
13	CML-O	RD+	Receiver Data Output	
14		VeeR	Module Receiver Ground	1
15		VccR	Module Receiver 3.3 V Supply	
16		VccT	Module Receiver 3.3 V Supply	
17		VeeT	Module Transmitter Ground	1
18	CML-I	TD+	Transmitter Non-Inverted Data Input	
19	CML-I	TD-	Transmitter Inverted Data Input	
20		VeeT	Module Transmitter Ground	1

### Notes:

- 1. Module ground pins GND are isolated from the module case.
- 2. Shall be pulled up with 4.7K-10K $\Omega$  to a voltage between 3.15V and 3.45V on the host board.

## **Pin Definitions**



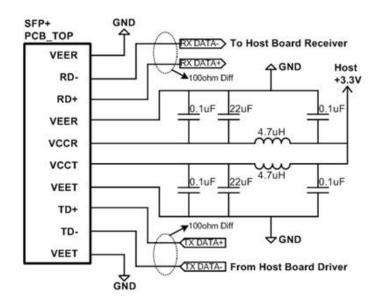
Pin Definitions

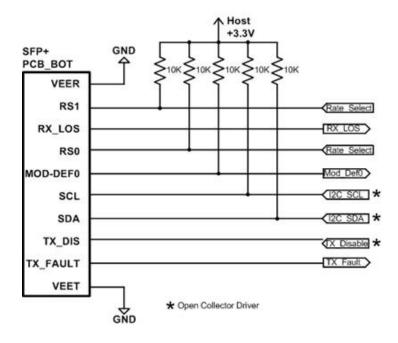
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### PSFP28-AOCXX

## **Recommended Interface Circuit**

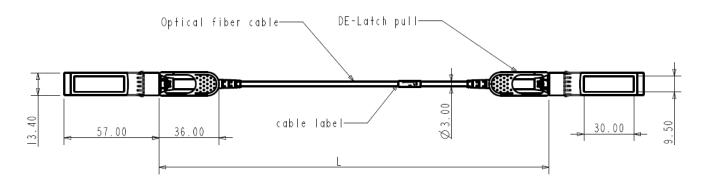


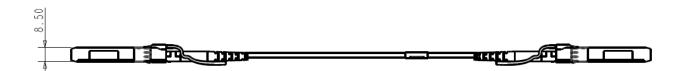


**Recommended Interface Circuit** 



## **Mechanical Dimensions**





**Mechanical Specifications**