PSFP28-2733-10F
PSFP28-3327-10F

25.78Gbps SFP28 Bi-Directional 10KM Hot Pluggable, Simplex LC, Single Mode PSFP28-2733-10F & PSFP28-3327-10F

Features

- Supports up to 25.78Gbps bit rates
- Hot-pluggable SFP+ footprint
- PSFP28-2733-10F : 1270nm DFB laser and PIN photodiode
- PSFP28-3327-10F: 1330nm DFB laser and PIN photodiode
- Up to 10km for SMF transmission
- Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature:
  Standard: 0 to +70°C

Applications

- 25GBASE-LR

Ordering information

Part Number	Product Description
PSFP28-2733-10F	SFP28 BIDI 25Gbps, 1270nm/1330nm, 10km, 0ºC ~ +70ºC
PSFP28-3327-10F	SFP28 BIDI 25Gbps, 1330nm/1270nm, 10km, 0ºC ~ +70ºC

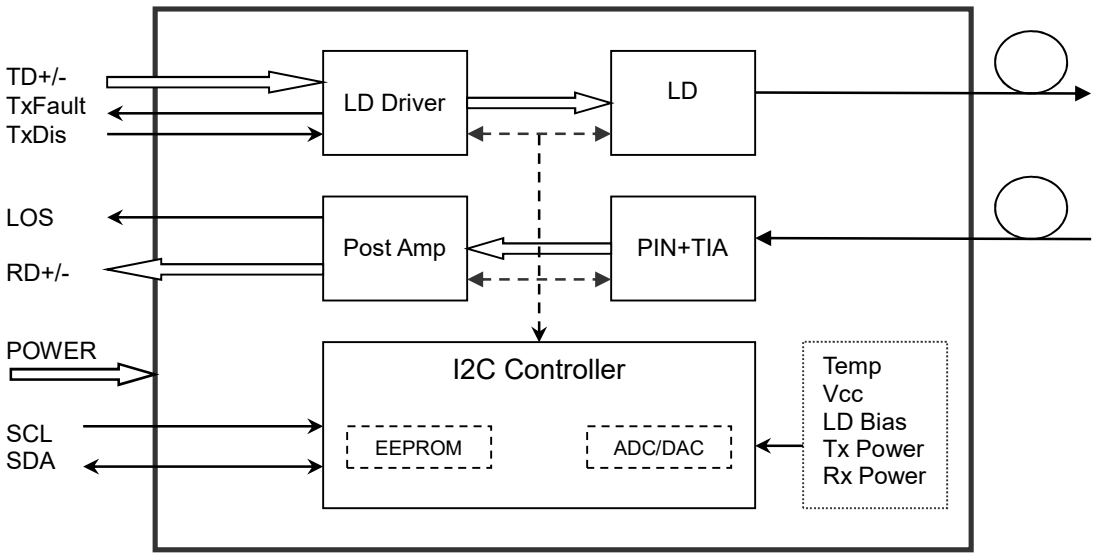
Overview

The SFP28 transceivers are high performance, cost effective modules supporting data rate of 25.78Gbps and 10km transmission distance with SMF.

The transceiver consists of three sections: a DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.

Transceiver Functional Diagram



Transceiver Functional Diagram

Absolute Maximum Ratings

Parameters	Symbol	Min.	Max.	Unit
Supply Voltage	V_{CC}	-0.5	4.5	V
Storage Temperature	Tc	-40	85	°C
Operating Case Temperature	Tc	0	70	°C
Relative Humidity	RH	5	85	%

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Tc	0		70	°C
Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	Icc			400	mA
Data Rate			25.78		Gbps

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Optical and Electrical Characteristics

PSFP28-2733-10F

Parameter		Symbol	Min	Typical	Max	Unit	Notes
Transmitter							
Centre Wavelength		λc	1260	1270	1280	nm	
Spectral Width（-20dB）		$\Delta\lambda$			1	nm	
Side-Mode Suppression Ratio		SMSR	30	-		dB	
Average Output Power		P_{out}	0		6	dBm	1
Extinction Ratio		ER	3.5			dB	
Data Input Swing Differential		V_{IN}	180		850	mV	2
Input Differential Impedance		Z_{IN}	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
	Enable		0		0.8	V	
TX Fault	Fault		2.0		Vcc	V	
	Normal		0		0.8	V	
Receiver							
Centre Wavelength		λc	1320	1330	1340	nm	
Receiver Sensitivity					-13.3	dBm	3
Receiver Overload					2	dBm	3
LOS De-Assert		LOS_D			-15	dBm	
LOS Assert		LOS_A	-30			dBm	
LOS Hysteresis			0.5			dB	
Data Output Swing Differential		V_{out}	300		900	mV	4
LOS		High	2.0		Vcc	V	
		Low			0.8	V	

Notes:
1. The optical power is launched into SMF
2. PECL input, internally AC-coupled and terminated
3. Measured with a PRBS 2^{31}-1 test pattern @25.78Gbps, BER ≤5×10^{-5}
4. Internally AC-coupled

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Parameter		Symbol	Min	Typical	Max	Unit	Notes
Transmitter							
Centre Wavelength		λc	1320	1330	1340	nm	
Spectral Width（-20dB）		$\Delta\lambda$			1	nm	
Side-Mode Suppression Ratio		SMSR	30	-		dB	
Average Output Power		P_{out}	0		6	dBm	1
Extinction Ratio		ER	3.5			dB	
Data Input Swing Differential		V_{IN}	180		850	mV	2
Input Differential Impedance		Z_{IN}	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
	Enable		0		0.8	V	
TX Fault	Fault		2.0		Vcc	V	
	Normal		0		0.8	V	
Receiver							
Centre Wavelength		λc	1260	1270	1280	nm	
Receiver Sensitivity					-13.3	dBm	3
Receiver Overload					2	dBm	3
LOS De-Assert		LOS_D			-15	dBm	
LOS Assert		LOS_A	-30			dBm	
LOS Hysteresis			0.5			dB	
Data Output Swing Differential		V_{out}	300		900	mV	4
LOS		High	2.0		Vcc	V	
		Low			0.8	V	

Notes:

1. The optical power is launched into SMF
2. PECL input, internally AC-coupled and terminated
3. Measured with a PRBS 2^{31}-1 test pattern @25.78Gbps, BER ≤5×10^{-5}
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Timing and Electrical

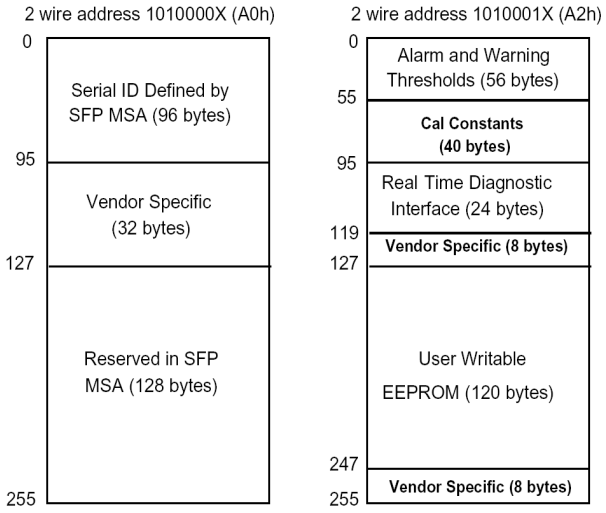
Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			2	ms
Tx Disable Assert Time	t_off			100	µs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	µs
Tx Disable To Reset	t_reset	10			µs
LOS Assert Time	t_loss_on			100	µs
LOS De-assert Time	t_loss_off			100	µs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V_H	2		Vcc	V
MOD_DEF (0:2)-Low	V_L			0.8	V

Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Transceiver Functional Diagram

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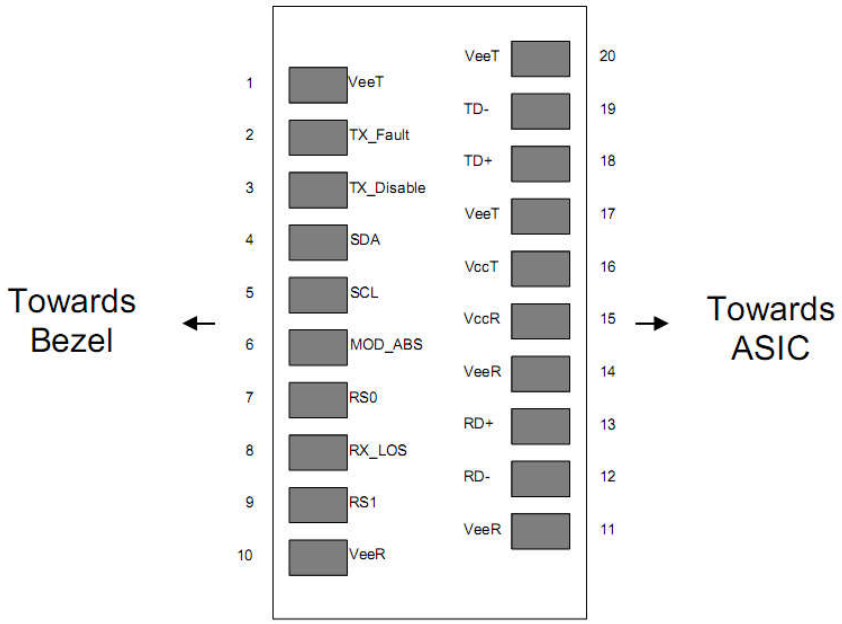
Pin Descriptions



Electrical Pin-out Details

Pin	Signal Name	Description	Plug Seq.	Notes
1	VEET	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V_{EER}	Receiver ground	1	
11	V_{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V_{EER}	Receiver ground	1	
15	VCCR	Receiver Power Supply	2	

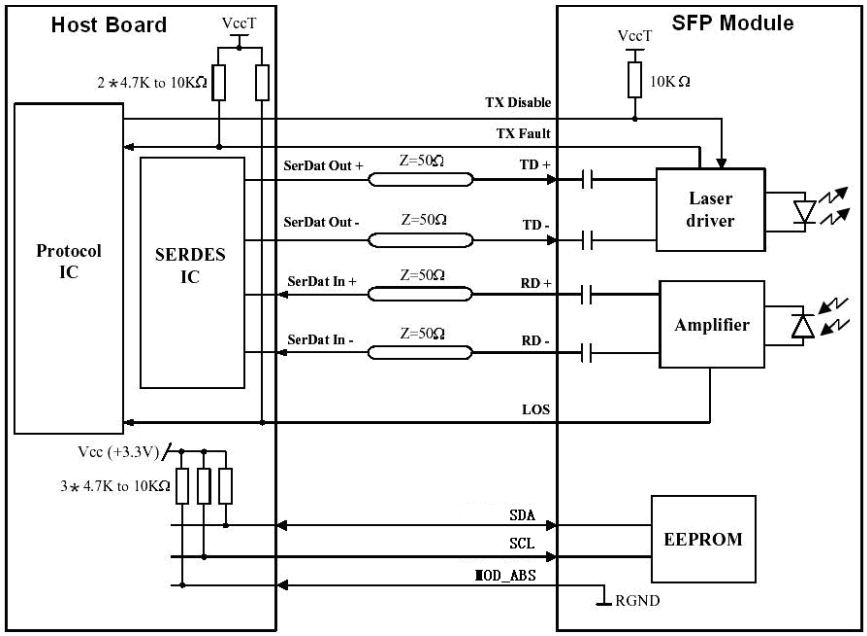
16	VCCT	Transmitter Power Supply	2	
17	V_{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	V_{EET}	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

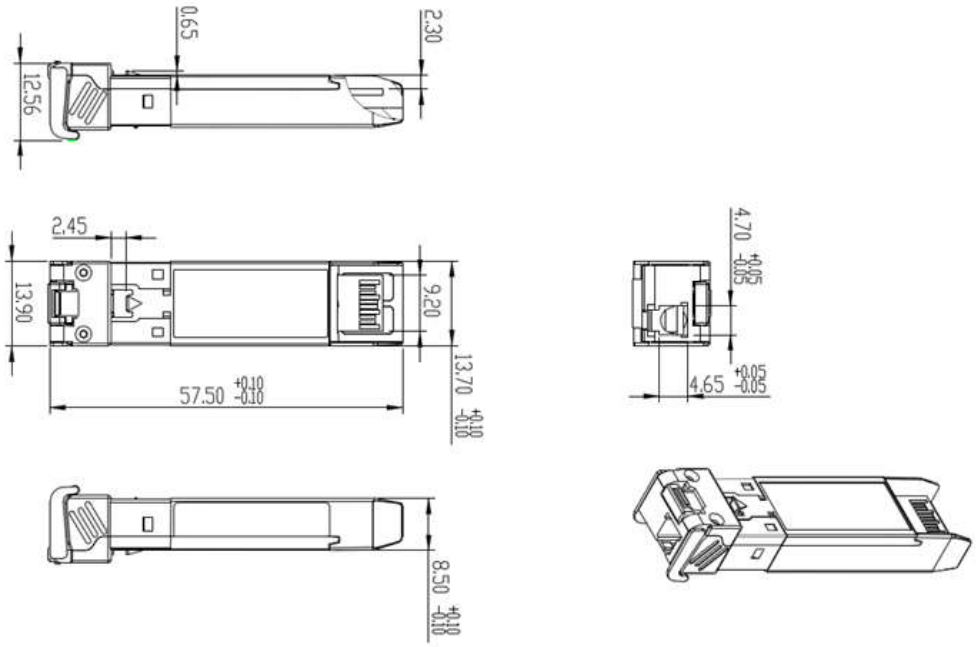
1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

Recommended Interface Circuit



Recommended Interface Circuit

Mechanical Dimension



Key Mechanical Specifications