

PCFP-24-334178133F/PCFP-24-343278133F

DATASHEET

DESCRIPTION:

PCFP-24-334178133F & PCFP-24-343278133F are 2 single fiber duplex transmissions in current SFP form factor. There it could double the port density of current equipment or line card. This module is designed for Single-Mode single fiber, operates at the normal wavelength of 1310/1490nm.Standard AC coupled CML for high speed signal and LVTTL control and monitor signals. The transmitter section incorporates FP or DFB and driver IC with temperature compensation and automatic power control circuit, which make the transmitter section output power and Extinction ration stabled in operation temperature. The receiver section incorporates an efficient InGaAs photodiode and transimpedance with AGC for wide dynamic range.

FEATURES:

- Two Bi-Directional Transceivers in One SFP Package
- Compliant with CSFP MSA 2.0 (Option 2)
- Up to 1.25Gb/s Data Links
- Hot-Pluggable Capability
- Up to 20 km on 9/125µm SMF
- Single +3.3V Power Supply
- Built-in Filter WDM
- Monitoring Interface Compliant with SFF-8472
- Isolation > 30dB, Cross Talk < -45dB
- Metal Enclosure, Excellent EMI & ESD Protection
- Compliant with Specifications for IEEE802.3ah
- Compliant with Bellcore TA-NWT-000983
- Eye Safety Designed to Meet Laser Class1, Compliant with IEC60825-1
- Operating temperature range: 0°C to 70°C
- RoHS Compliant

APPLICATIONS:

- Gigabit Ethernet
- Switch to Switch Interface
- WDM Application



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SPECIFICATIONS:

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typical	Max.	Unit	
Storage Temperature		Ts	-40		+85	°C
Supply Voltage		V _{CC} T, R	-0.5		4	V
Relative Humidity		RH	0		85	%
Case Operating Temperature Commercial		Тор	0		70	·C

Recommended Operating Environment:

Parameter	Symbol	Min.	Typical	Max.	Unit	
Case operating Temperature	Commercial	Тор	0		70	·C
Supply Voltage		V _{CCT, R}	3.0		3.6	V
Power Supply Rejection			100			mV _{P-P}



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Electrical Characteristics ($T_{OP} = 0$ to 70 °C, VCC = 3.0 to 3.60 Volts)

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Supply Voltage	Vcc	3.0	3.30	3.60	V	
Supply Current	Icc		280	400	mA	
Inrush Current	I _{surge}			Icc+30	mA	
Maximum Power	P _{max}			1.0	W	
Transmitter Section: (Each Channel)	-		-		_	
Input differential impedance	R _{in}	90	100	110		
Single ended data input swing	V _{in PP}	200		1200	mVp-p	
Transmit Disable Voltage	V_D	Vcc - 1.3		Vcc	V	2
Transmit Enable Voltage	V_{EN}	Vee		Vee+ 0.8	V	
Transmit Disable Assert Time	$T_{dessert}$			10	us	
Receiver Section: (Each Channel)						
Single ended data output swing	Vout,pp	300		1000	mv	3
Data output rise time	t _r			150	ps	4
Data output fall time	t _f			150	ps	4
LOS Fault	V _{losfault}	Vcc - 0.5		$V_{\text{CC_host}}$	V	5
LOS Normal	V _{los norm}	V _{ee}		V _{ee} +0.5	V	5
Power Supply Rejection	PSR	100			mVpp	6
Deterministic Jitter Contribution	RXΔDJ			51.7	ps	7
Total Jitter Contribution	RXΔTJ			122.4	ps	

Note:

- 1. AC coupled.
- 2. Or open circuit.
- 3. Into 100 ohm differential termination.
- 4. 20 80 %
- 5. LOS is LVTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
- 6. All transceiver specifications are compliant with a power supply sinusoidal modulation of 20 Hz to 1.5MHz up to specified value applied through the power supply filtering network shown on page 23 of the Small Form-factor Pluggable (SFP) Transceiver Multi-Source Agreement (MSA), September 14, 2000.
- 7. Measured with DJ-free data input signal. In actual application, output DJ will be the sum of input DJ and. DJ.



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Optical Parameters (TOP = 0 to 70 °C, VCC = 3.00 to 3.60 Volts)

Param	Symbol	Min.	Typical	Max.	Unit	Note			
Transmitter Section: (E	ach Channel)								
	PCFP-24-334178133F	,	1260	1310	1360		1		
Center Wavelength	PCFP-24-343278133F	λ_{c}	1480	1490	1500	nm			
Connetwel Width	PCFP-24-334178133F	_			4		RMS		
Spectral Width	PCFP-24-343278133F	σ			1	nm	-20dB		
Optical Output Power		P _{out}	-9		-3	dBm	2		
Optical Rise/Fall Time		t _r / t _f			160	ps	3		
Extinction Ratio		ER	9			dB			
Deterministic Jitter Co	ntribution	TXΔDJ			56.5	ps	4		
Total Jitter Contributio	n	TXΔTJ			119	ps			
Eye Mask for Optical O	Eye Mask for Optical Output			Compliant with Eye Mask Defined in IEEE 802.3 standard					
Relative Intensity Nois	e	RIN			-120	dB/Hz			
Receiver Section: (Eacl	n Channel)								
Optical Input	PCFP-24-334178133F	- 1	1480	1490	1500	nm			
Wavelength	PCFP-24-343278133F	• \(\lambda_c\)	1260	1310	1360				
Optical Input Power		P _{in}	-23		-3	dBm	5.6		
Receiver Reflectance			12			dB			
Receiver Overload		Pol			-3	dBm	5.6		
RX Sensitivity		Sen			-23	dBm	5.6		
RX_LOS Assert		LOS _A	-33			dBm			
RX_LOS Deassert		LOS _D			-23	dBm			
RX_LOS Hysteresis		LOS _H		2	2.5	dB			
General Specifications	General Specifications								
Data Rate		BR	1062		1250	Mb/s			
Bit Error Rate		BER			10 ⁻¹²				
Max. Supported Link Lo SMF@1.25G	Max. Supported Link Length on 9/125µm SMF@1.25G			20		km	7		
Total System Budget		LB	13			dB	8		

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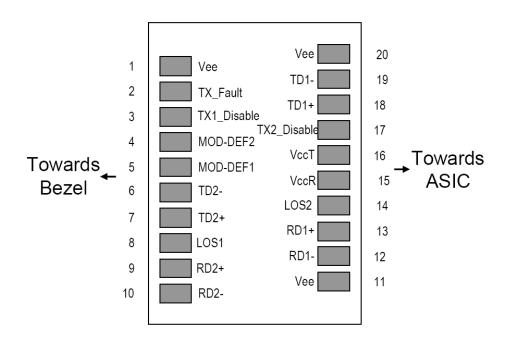
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Digital Diagnostic Monitor Characteristics

The following digital diagnostic characteristics are defined over the Recommended Operating Environment unless otherwise specified. It is compliant to SFF8472 Rev10.2 with internal calibration mode. For external calibration mode please contact our sales stuff.

Parameter	Symbol	Min.	Max.	Unit
Temperature monitor absolute error	DMI_Temp	-3	3	degC
Laser power monitor absolute error	DMI_TX	-3	3	dB
RX power monitor absolute error	DMI_RX	-3	3	dB
Supply voltage monitor absolute error	DMI_VCC	-0.1	0.1	٧
Bias current monitor absolute error	DMI_Ibias	-10%	10%	mA

Pin Assignment:





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Pin Description:

Pin	Name	Function	Plug Seq.	Notes
Num.				
1	Vee	Transceiver Ground	1	Note1
2	TX_Fault	Transmitter Fault indication	3	Note2
3	TX1_DISABLE	1_DISABLE Transmitter Disable of Ch1;Turns off transmitter laser output of Ch1		Note3
4	MOD-DEF2	2-wire Serial Interface Data Line (SDA)	3	Note4
5	MOD-DEF1	2-wire Serial Interface Clock (SCL)	3	Note4
6	TD2-	Inverted Transmit Data Input of Ch2	3	Note5
7	TD2+	Transmit Data Input of Ch2	3	Note5
8	LOS1	Loss of Signal for ch1	3	Note6
9	RD2+	Received Data Output of Ch2	3	Note7
10	RD2-	Inverted Received Data Output of Ch2	3	Note7
11	Vee	Transceiver Ground	1	Note1
12	RD1-	Inverted Received Data Output of Ch1	3	Note7
13	RD1+	Received Data Output of Ch1	3	Note7
14	LOS2	Loss of signal for ch2	3	Note6
15	VCCR	Receiver Power	2	Note8
16	VCCT	Transmitter Power	2	Note8
17	TX2_DISABLE	TX2_DISABLE Transmitter Disable of Ch2;Turns off transmitter laser output of Ch2		Note3
18	TD1+	Transmit Data Input of Ch1	3	Note5
19	TD1-	Inverted Transmit Data Input of Ch1	3	Note5
20	Vee	Transceiver Ground	1	Note1

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- 1) VeeR and VeeT may be internally connected within the SFP module.
- 2)TX_Fault is internally OR out put for TX fault conditions in either channel 1 or channel 2. In order to determine which channel is at fault, the Host can read the internal memory bits for status:

- Bit2 in (A2h:110) for TX1 fault



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- Bit2 in (B2h:110) for TX2 fault

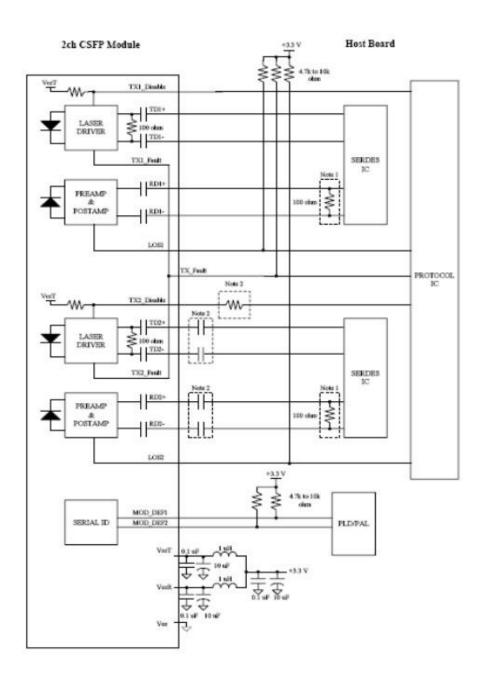
Tx_Fault is an open collector/drain output, which should be pulled up with a $4.7k - 10k\Omega$ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind at either channel. Low indicates normal operation. In the low state, the output will be pulled to < 0.4V.

- 3) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7 °C 10 K resistor. It's states are: Low (0 to 0.8V): Transmitter on (>0.8, < 2.0V): Undefined High (2.0 to 3.465V): Transmitter Disabled Open: Transmitter Disabled.
- 4) Mod-Def 1,2. These are the module definition pins. They should be pulled up with a 4.7K to 10K resistor on the host board. The pull-up voltage shall be VccT or VccR Mod-Def 1 is the clock line of two wire serial interface for serial ID Mod-Def 2 is the data line of two wire serial interface for serial ID.
- 5) TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100 differential terminations inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 500 to 2400 mV (250 to 1200 mV single-ended), though it is recommended that values between 500 and 1200 mV differential (250 to 600 mV single-ended) be used for best EMI performance.
- 6) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a $4.7K-10K\Omega$ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.4V.
- 7) RD-/+: These are the differential receiver outputs. They are AC coupled 100 differential lines which should be terminated with 100 (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 370 and 2000 mV differential (185 to 1000 mV single ended) when properly terminated.
- 8) VccR and VccT are the receiver and transmitter power supplies. They are defined as $3.3V \pm 5\%$ at the SFP connector pin. Maximum supply current is 300 mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1Ω should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.



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Recommended Circuit:





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Serial ID Memory Contents: (A0h CH1, B0h CH2)

Data Address	Length (Byte)	Name of Length	Description and Contents			
Base ID Fields						
0	1	Identifier	Type of Serial transceiver (03h=SFP)			
1	1	Reserved	Extended identifier of type serial transceiver (04h)			
2	1	Connector	Code of optical connector type (07=LC)			
3-10	8	Transceiver	SONET			
11	1	Encoding	SONET Scrambled			
12	1	BR,Nominal	Nominal baud rate, unit of 100Mbps			
13	1	Reserved	(0000h)			
14	1	Length(9um,km)	Link length supported for 9/125um fiber, units of km			
15	1	Length(9um)	Link length supported for 9/125um fiber, units of 100m			
16	1	Length(50um)	Link length supported for 50/125um fiber, units of 10m			
17	1	Length(62.5um)	Link length supported for 62.5/125um fiber, units of 10m			
18	1	Length(Copper)	Link length supported for copper, units of meters			
19	1	Reserved				
20-35	16	Vendor Name	SFP vendor name:			
36	1	Reserved				
37-39	3	Vendor OUI	SFP transceiver vendor OUI ID			
40-55	16	Vendor PN	Part Number: "PCFPXXXXXXXX" (ASCII)			
56-59	4	Vendor rev	Revision level for part number			
60-61	2	Wavelength	Laser wavelength			
62	1	Reserved				
63	1	CCID	Least significant byte of sum of data in address 0-62			
Extended ID	Fields	-				
64-65	2	Option	Indicates which optical SFP signals are implemented(001Ah = LOS, TX_FAULT, TX_DISABLE all supported)			
66	1	BR, max	Upper bit rate margin, units of %			
67	1	BR, min	Lower bit rate margin, units of %			
68-83	16	Vendor SN	Serial number (ASCII)			
84-91	8	Date code	Manufacturing date code			
92	1	Diagnostic Type	Diagnostics			
93	1	Enhanced Options	Diagnostics			
94	1	SFF-8472	Diagnostics			
95	1	CCEX	Check code for the extended ID Fields (addresses 64 to 94)			
Vendor Speci	ific ID Fields	L				
96-127	32	Readable	Vendor specific date, read only			



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Diagnostics Memory Contents: (A2h CH1,B2h CH2)

Data Address	Length (Byte)	Name of Length	Description and Contents		
Diagnostic and	d control/sta	atus fields			
0-39	40	A/W Thresholds	Diagnostic Flag Alarm and Warning Thresholds		
40-55	16	Unallocated			
56-91	16	Ext Cal Constants	Diagnostic calibration constants for optional External Calibration		
92-94	3	Unallocated			
95	1	CC_DMI	Check code for Base Diagnostic Fields (addresses 0 to 94)		
96-105	10	Diagnostics	Diagnostic Monitor Data (internally or externally calibrated)		
106-109	4	Unallocated			
110	1	Status/Control	Optional Status and Control Bits		
111	1	Reserved	Reserved for SFF-8079		
112-113	2	Alarm Flags	Diagnostic Alarm Flag Status Bits		
114-115	2	Unallocated			
116-117	2	Warning Flags	Diagnostic Warning Flag Status Bits		
118-119	2	Ext Status/Control	Extended module control and status bytes		
General use fields					
120-127	8	Vendor Specific	Vendor specific memory addresses		
128-247	120	User EEPROM	User writable non-volatile memory		
248-255	8	Vendor Control	Vendor specific control addresses		



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Mechanical Dimensions:

